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1.Generally, the refreshing rate of dynamic RAMs is approximately once in

a) Two micro seconds

b) Two milli seconds

c) Sixty four milli seconds

d) Two micro seconds

Ans : b

2.In case of, Zero-address instruction method the operands are stored in

a) Registers

b) Accumulators

c)Pushdown stack

d)Cache

Ans: c

3. The memory address range to which RAM will respond

a) 0000 H to 1 FFF H

b) 0000 H to 5FFF H

c) 4000 H to 5FFF H

d) 3000 H to FFFF H

Ans: c

4. The ultimate goal of a compiler is to

a) Reduce the clock cycles for a programming task

b) Reduce the size of the object code

c) Be versatile

d)Be able to detect even the smallest of errors

Ans: a

5. The inputs in the PLD is given through

a)NAND gates

b)OR gates

c)NOR gates

d)AND gates

Ans : d

6. Don’t care terms should be used along with the terms that are present in

a)Minterms

b)Maxterm  
c)K-Map  
d)Latches  
Ans: a

7. Using the transformation method you can realize any POS realization of OR AND with only.

a)XOR  
b)NAND  
c)AND  
d)NOR

Ans: d

8.How many bytes of bit addressable memory is present in 8051 based micro controllers?  
a) 8 bytes  
b) 32 bytes  
c) 16 bytes  
d) 128 bytes

Ans: c

9.What is the correct order of priority that is set after a controller gets reset?  
a) TxD/RxD > T1 > T0 >EX1 > EX0  
b) TxD/RxD < T1 < T0<EX1 < EX0  
c) EX0 > T0 > EX1 >T1> TxD/RxD  
d) EX0 < T0 < EX1 < T1 < TxD/RxD

Ans: c

10.Which of the following registers are not bit addressable?  
a)SCON  
b)PCON  
c)A  
d) PSW

Ans: b